

WEST[Generate Collection](#)[Print](#)**Search Results - Record(s) 1 through 10 of 45 returned.** **1. Document ID: US 20020199040 A1**

L3: Entry 1 of 45

File: PGPB

Dec 26, 2002

PGPUB-DOCUMENT-NUMBER: 20020199040

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020199040 A1

TITLE: High speed communications device/system

PUBLICATION-DATE: December 26, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Irwin, David S.	Kenthurst	NH	AU	
Lillywhite, Colin S.	Hanover		US	
Betzis, Andrew	Blakehurst		AU	
McDougall, Mark	Sans Souci		AU	

US-CL-CURRENT: 710/22

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC
Draw Desc		Image									

 2. Document ID: US 20020185725 A1

L3: Entry 2 of 45

File: PGPB

Dec 12, 2002

PGPUB-DOCUMENT-NUMBER: 20020185725

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020185725 A1

TITLE: Low profile multi-IC chip package connector

PUBLICATION-DATE: December 12, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Moden, Walter L.	Meridian	ID	US	
King, Jerrold L.	Morgan Hill	CA	US	
Brooks, Jerry M.	Caldwell	ID	US	

US-CL-CURRENT: 257/686

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMPC
Draw Desc Image											

3. Document ID: US 20020182772 A1

L3: Entry 3 of 45

File: PGPB

Dec 5, 2002

PGPUB-DOCUMENT-NUMBER: 20020182772
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020182772 A1

TITLE: Low profile multi-IC chip package connector

PUBLICATION-DATE: December 5, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Moden, Walter L.	Meridlan	ID	US	
King, Jerrold L.	Morgan Hill	CA	US	
Brooks, Jerry M.	Caldwell	ID	US	

US-CL-CURRENT: 438/109; 29/832

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMPC
Draw Desc Image											

4. Document ID: US 20020084520 A1

L3: Entry 4 of 45

File: PGPB

Jul 4, 2002

PGPUB-DOCUMENT-NUMBER: 20020084520
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020084520 A1

TITLE: Low profile multi-IC chip package connector

PUBLICATION-DATE: July 4, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Moden, Walter L.	Meridian	ID	US	
King, Jerrold L.	Morgan Hill	CA	US	
Brooks, Jerry M.	Caldwell	ID	US	

US-CL-CURRENT: 257/686

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMPC
Draw Desc Image											

5. Document ID: US 20020083255 A1

L3: Entry 5 of 45

File: PGPB

Jun 27, 2002

PGPUB-DOCUMENT-NUMBER: 20020083255
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020083255 A1

TITLE: Method and apparatus using switches for point to point bus operation

PUBLICATION-DATE: June 27, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Greeff, Roy	Boise	ID	US	
Ovard, David	Meridian	ID	US	
Lee, Terry R.	Boise	ID	US	

US-CL-CURRENT: 710/305

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Drawn-Desc. Image									

KMD

 6. Document ID: US 20020080541 A1

L3: Entry 6 of 45

File: PGPB

Jun 27, 2002

PGPUB-DOCUMENT-NUMBER: 20020080541
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020080541 A1

TITLE: Removable battery pack for a cache card

PUBLICATION-DATE: June 27, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Bunker, M. Scott	Tomball	TX	US	
Sabotta, Michael L.	Cypress	TX	US	
Grady, John R.	Houston	TX	US	

US-CL-CURRENT: 361/72

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Drawn Desc. Image									

KMD

 7. Document ID: US 20020059492 A1

L3: Entry 7 of 45

File: PGPB

May 16, 2002

PGPUB-DOCUMENT-NUMBER: 20020059492
PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020059492 A1

TITLE: Method and apparatus for adapting a card for use with multiple protocols

PUBLICATION-DATE: May 16, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Sabotta, Michael L.	Cypress	TX	US	
Bunker, M. Scott	Tomball	TX	US	

US-CL-CURRENT: 710/301

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Descr Image									

KMDC

8. Document ID: US 20020058444 A1

L3: Entry 8 of 45

File: PGPB

May 16, 2002

PGPUB-DOCUMENT-NUMBER: 20020058444

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020058444 A1

TITLE: DIMM connector accomodating sideband signals for battery status and/or control

PUBLICATION-DATE: May 16, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Sabotta, Michael L.	Cypress	TX	US	
Bunker, M. Scott	Tomball	TX	US	

US-CL-CURRENT: 439/692

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Descr Image									

KMDC

9. Document ID: US 20020057072 A1

L3: Entry 9 of 45

File: PGPB

May 16, 2002

PGPUB-DOCUMENT-NUMBER: 20020057072

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020057072 A1

TITLE: Battery gauge using a resettable decrementer in a DIMM

PUBLICATION-DATE: May 16, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Bunker, M. Scott	Tomball	TX	US	

US-CL-CURRENT: 320/132

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMC
Draw Desc Image										

10. Document ID: US 20020056018 A1

L3: Entry 10 of 45

File: PGPB

May 9, 2002

PGPUB-DOCUMENT-NUMBER: 20020056018
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020056018 A1

TITLE: Method and apparatus implementing a tuned stub SCSI topology

PUBLICATION-DATE: May 9, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Schumacher, Matthew J.	Cypress	TX	US	
Bunker, M. Scott	Tomball	TX	US	

US-CL-CURRENT: 710/300

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMC
Draw Desc Image										

Term	Documents
CONNECTOR\$	0
CONNECTOR	253729
CONNECTORABILITY	6
CONNECTORACCORDING	1
CONNECTORAGP	1
CONNECTORAIL	2
CONNECTORAND	4
CONNECTORARCHITECTURE	2
CONNECTORASSEMBLY	1
CONNECTORB	1
CONNECTORBEING	1
(L2 AND (CONNECTOR\$ OR SOCKET\$1)).USPT,PGPB.	45

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L3: Entry 11 of 45

File: PGPB

Mar 21, 2002

PGPUB-DOCUMENT-NUMBER: 20020035623
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020035623 A1

TITLE: Method and apparatus for operating the internet protocol over a high-speed serial bus

PUBLICATION-DATE: March 21, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Lawande, Sachin S.	Mount Prospect	IL	US	
Ling, Salim	Skokie	IL	US	

US-CL-CURRENT: 709/221; 709/220

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [KMD](#) |
[Draw Desc](#) | [Image](#)

 12. Document ID: US 20010020740 A1

L3: Entry 12 of 45

File: PGPB

Sep 13, 2001

PGPUB-DOCUMENT-NUMBER: 20010020740
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20010020740 A1

TITLE: Low profile multi-IC chip package connector

PUBLICATION-DATE: September 13, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Moden, Walter L.	Meridian	ID	US	
King, Jerrold L.	Morgan Hill	CA	US	
Brooks, Jerry M.	Caldwell	ID	US	

US-CL-CURRENT: 257/685; 257/686, 257/723, 257/724

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMC
Draw Desc Image										

13. Document ID: US 20010006829 A1

L3: Entry 13 of 45

File: PGPB

Jul 5, 2001

PGPUB-DOCUMENT-NUMBER: 20010006829
PGPUB-FILING-TYPE: new-utility
DOCUMENT-IDENTIFIER: US 20010006829 A1

TITLE: Low profile multi-IC chip package connector

PUBLICATION-DATE: July 5, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Moden, Walter L.	Meridian	ID	US	
King, Jerrold L.	Morgan Hill	CA	US	
Brooks, Jerry M.	Caldwell	ID	US	

US-CL-CURRENT: 438/109

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMC
Draw Desc Image										

14. Document ID: US 6545895 B1

L3: Entry 14 of 45

File: USPT

Apr 8, 2003

US-PAT-NO: 6545895
DOCUMENT-IDENTIFIER: US 6545895 B1

TITLE: High capacity SDRAM memory module with stacked printed circuit boards

DATE-ISSUED: April 8, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Li; Che-yu	Ithaca	NY		
Moriarty; Sharon L.	Mountain View	CA		

US-CL-CURRENT: 365/52; 365/51, 365/63

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMC
Draw Desc Image										

15. Document ID: US 6530033 B1

L3: Entry 15 of 45

File: USPT

Mar 4, 2003

US-PAT-NO: 6530033

DOCUMENT-IDENTIFIER: US 6530033 B1

TITLE: Radial arm memory bus for a high availability computer system

DATE-ISSUED: March 4, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Raynham; Michael B.	Los Gatos	CA		
Wiggers; Hans A.	Saratoga	CA		

US-CL-CURRENT: 714/5; 365/189.05, 365/230.06

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMPC
Draw Desc Image										

 16. Document ID: US 6510100 B2

L3: Entry 16 of 45

File: USPT

Jan 21, 2003

US-PAT-NO: 6510100

DOCUMENT-IDENTIFIER: US 6510100 B2

TITLE: Synchronous memory modules and memory systems with selectable clock termination

DATE-ISSUED: January 21, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Grundon; Steven	Jericho	VT		
Kellogg; Mark	Essex Junction	VT		

US-CL-CURRENT: 365/233; 365/204

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMPC
Draw Desc Image										

 17. Document ID: US 6486546 B2

L3: Entry 17 of 45

File: USPT

Nov 26, 2002

US-PAT-NO: 6486546

DOCUMENT-IDENTIFIER: US 6486546 B2

TITLE: Low profile multi-IC chip package connector

DATE-ISSUED: November 26, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Moden; Walter L.	Meridian	ID		
King; Jerrold L.	Morgan Hill	CA		
Brooks; Jerry M.	Caldwell	ID		

US-CL-CURRENT: 257/686; 257/685, 257/723, 257/724, 257/E25.023

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMC
Draw Desc Image										

 18. Document ID: US 6475831 B2

L3: Entry 18 of 45

File: USPT

Nov 5, 2002

US-PAT-NO: 6475831

DOCUMENT-IDENTIFIER: US 6475831 B2

TITLE: Methods for a low profile multi-IC chip package connector

DATE-ISSUED: November 5, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Moden; Walter L.	Meridian	ID		
King; Jerrold L.	Morgan Hill	CA		
Brooks; Jerry M.	Caldwell	ID		

US-CL-CURRENT: 438/109; 257/E25.023, 438/106, 438/107, 438/110, 438/121

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMC
Draw Desc Image										

 19. Document ID: US 6469474 B2

L3: Entry 19 of 45

File: USPT

Oct 22, 2002

US-PAT-NO: 6469474

DOCUMENT-IDENTIFIER: US 6469474 B2

TITLE: Battery gauge using a resettable decrementer in a DIMM

DATE-ISSUED: October 22, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Bunker; M. Scott	Tomball	TX		

US-CL-CURRENT: 320/132

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc Image									KMC

20. Document ID: US 6449308 B1

L3: Entry 20 of 45

File: USPT

Sep 10, 2002

US-PAT-NO: 6449308

DOCUMENT-IDENTIFIER: US 6449308 B1

TITLE: High-speed digital distribution system

DATE-ISSUED: September 10, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Knight, Jr.; Thomas F.	Belmont	MA		
Marketkar; Nandu J.	Boylston	MA		

US-CL-CURRENT: 375/212; 370/438, 375/220, 375/257

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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